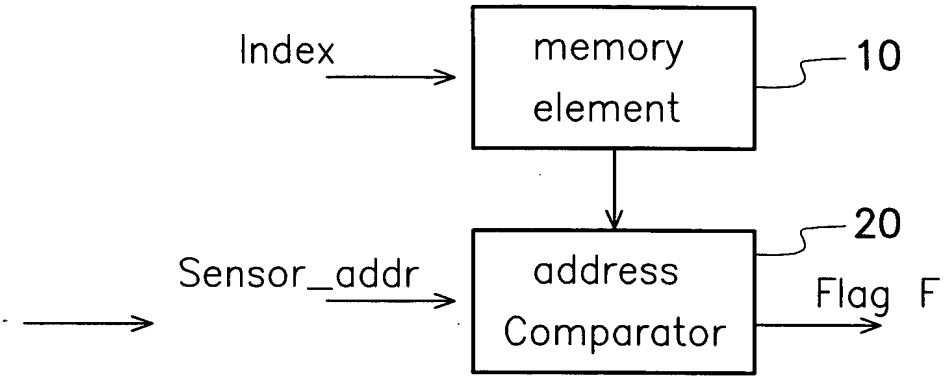
Index
memory element
10
20
Sensor_addr
address Comparator
Flag F

FIG. 1

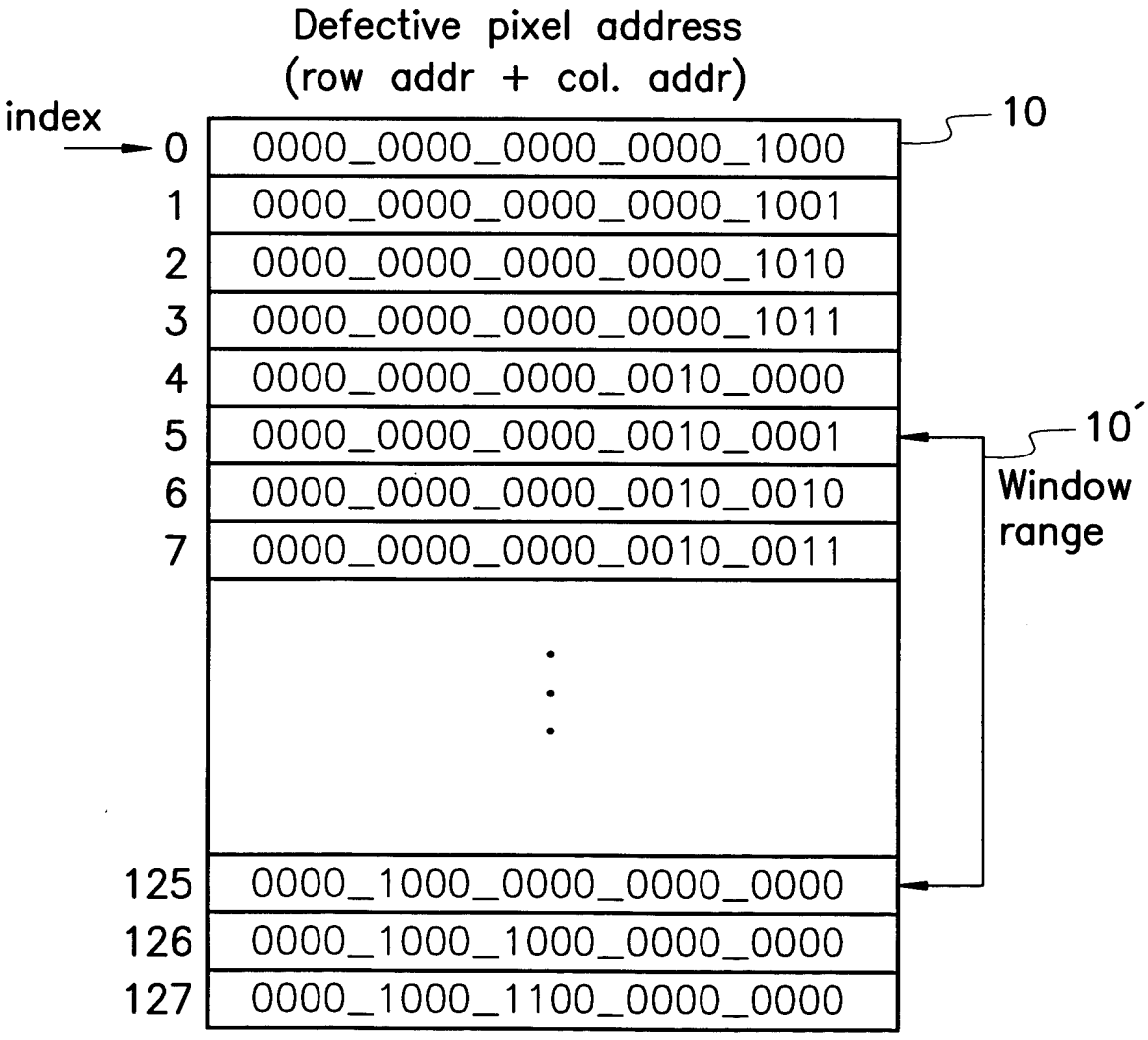
Defective pixel address
(row addr + col. addr)
index
10
0 0000_0000_0000_0000_1000
1 0000_0000_0000_0000_1001
2 0000_0000_0000_0000_1010
3 0000_0000_0000_0000_1011
4 0000_0000_0000_0010_0000
5 0000_0000_0000_0010_0001
6 0000_0000_0000_0010_0010
7 0000_0000_0000_0010_0011
10´
Window range
125 0000_1000_0000_0000_0000
126 0000_1000_1000_0000_0000
127 0000_1000_1100_0000_0000

FIG. 2

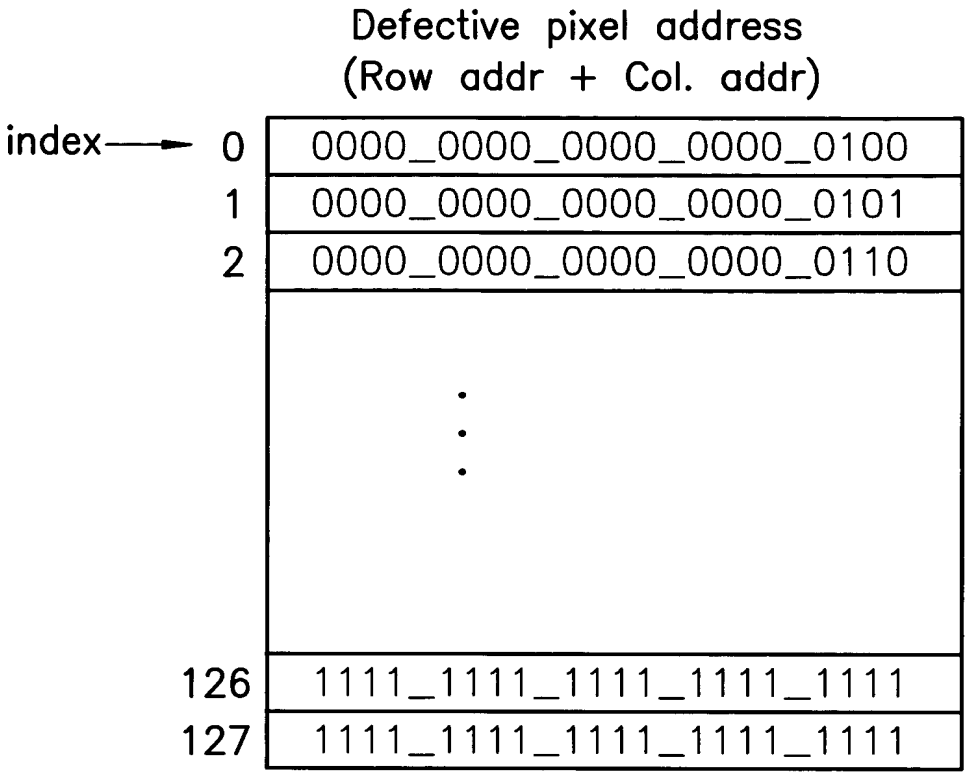
Defective pixel address
(Row addr + Col. addr)
index
0
1
2
126
127
0000_0000_0000_0000_0100
0000_0000_0000_0000_0101
0000_0000_0000_0000_0110
1111_1111_1111_1111_1111
1111_1111_1111_1111_1111

FIG. 3

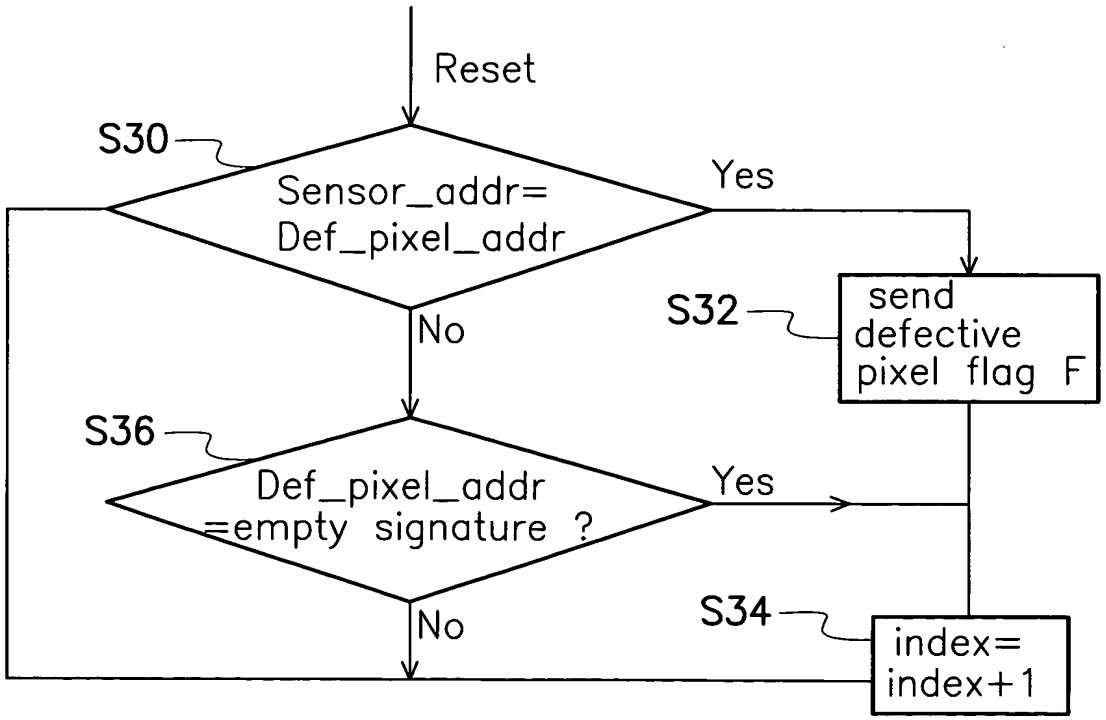
Reset
S30
Sensor_addr=
Def_pixel_addr
Yes
No
S32
send
defective
pixel flag F
S36
Def_pixel_addr
=empty signature ?
Yes
No
S34
index=
index+1

FIG. 4